

AMENDMENTS

In the Claims

Please cancel claims 1-14 without prejudice to file same in a continuation, divisional, continuation-in-part or co-pending application.

Applicants respectfully submit that no amendments have been made to the pending claims for the purpose of overcoming any prior art rejections that would restrict the literal scope of the claims or equivalents thereof.

PENDING CLAIMS AND STATUS THEREOF

Claims 1-14 (canceled)

15. (original allowed) A system for communications between two or more devices, said system comprising:

first device application logic adapted for sending transmit serial data at a serial clock rate and receiving receive serial data at the serial clock rate;

a first communications controller coupled to said first device application logic, said first communications controller encoding the transmit serial data and decoding the receive serial data on a communication circuit;

second device application logic adapted for receiving the transmit serial data at the serial clock rate and transmitting the receive serial data at the serial clock rate;

a second communications controller coupled to said second device application logic, said communications controller decoding the transmit serial data and encoding the receive serial data on the communication circuit;

said first and second communications controllers having a first programmable non-volatile memory for storing a first identification data string representative of the capabilities of the first device;

said second communications controllers having a second programmable non-volatile memory for storing a second identification data string representative of the capabilities of the second device; and

wherein the first and second identification data strings are transmitted and received between the first and second devices over the communications circuit.

16. (original allowed) The system according to claim 15, further comprising:

a first infrared transmitter and first encoder coupled to said first communications controller, wherein said first infrared transmitter and first encoder convert first transmit pulses from said first communications controller into first infrared light pulses;

a second infrared transmitter and second encoder coupled to said second communications controller, wherein said second infrared transmitter and second encoder convert second transmit pulses from said second communications controller into second infrared light pulses;

a first infrared receiver adapted for receiving the second infrared light pulses and converting the second infrared light pulses into first receive pulses;

a second infrared receiver adapted for receiving the first infrared light pulses and converting the first infrared light pulses into second receive pulses;

said first infrared receiver coupled to said first communications controller; and

said second infrared receiver coupled to said second communications controller.

17. (original allowed) The system according to claim 15, wherein said first and second device application logic are selected from the group consisting of a microcontroller, a microprocessor, digital signal processor, a programmable logic array and an application specific integrated circuit.

18. (original allowed) The system according to claim 15, wherein said first and second device application logic comprises a central processing unit, a random access memory and a read only memory.